#### ASPLOS '21

# STATISTICAL ROBUSTNESS OF MARKOV CHAIN MONTE CARLO ACCELERATORS

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### Sources of Uncertainty



 $D_{gt}$  = Ground truth  $D_{mdl}$  = Optimal from model  $D_{alg}$  = Learned/inferred  $D_{imp}$  = Implementation

[Hullermeier and Waegeman 2019]

#### Difficult to directly quantify implementation uncertainty...



### Solution: Statistical Robustness

Claim: "A probabilistic architecture should provide some measure (or guarantee) of statistical robustness."



- Modified methods for:
  - 1) high dimensionality

2) zero empirical variance

- No need to access ground truth
  - comparing with target quality (e.g. FP64)

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# Using the Three Pillars



#### **Methodology**

2 applications: Stereo Vision, Motion Estimation

2 Modes: Sampling, optimization



# Pillar 1: Sampling Quality (Effective Sample Size)



- Architectural optimizations: possibly reduce the independent samples
- ESS: number of independent samples drawn (lower value -> more iterations)
- Red pixels have var=0: no defined ESS -> Can't directly apply existing metrics
  - "Overall" ESS: omits var=0 pixels in software and the SPU, respectively, bias to software (yellow high ESS)
  - "Active" ESS: pixels with meaningful ESS (var> 0 in both software and SPU)





- How many iterations to converge?
  - Gelman-Rubin's  $\widehat{R}$ : variance Within (W) vs. Between (B) MCMC runs
  - Rule of thumb: < 1.1 is good (converged).
  - But, W=0 no definition -> Can't directly apply existing metrics
- ${\boldsymbol{\cdot}}$  New metric, Convergence Percentage based on Gelman-Rubin's  $\widehat{R}$
- SPU design needs 2x iterations

# Pillar 3: Goodness of Fit (RMSE) / End-Point Results



- Reference (per pixel) for RMSE: Mode of 10 software FP64 runs
- RMSE, End-point result quality (BP): comparable results (most whiskers overlap)
  - Confirms single-run result quality in slide 5.
  - FP64 not always same/better than SPU

#### Goodness of Fit: Jensen-Shannon Divergence (2-label case)



- Architectural optimizations:
- + good end-point results

• Co-

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- compromised statistical robustness
- Ir reducing effective speedups by 2x

al., ISCA 2018]

# Using the Three Pillars



#### How to remove the 2x overhead? - with minimum area/power overheads



# What If Only Using End-point Result Quality...

Power **†** 



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- Difficult to tell which is the best design
- Need three pillars to provide insights

### **DSE: Statistical Robustness**



#### Achieves statistical robustness comparable to FP64

- Probability bits 4->6, remove 2<sup>n</sup> approximation
- 19-bit LFSR is good.

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- Hardware resources ("p6"):
  - Modest increases: 20% area and 10% power

• much lower area/power vs. FP HW Copyright © Duke 2021

### Conclusion



- We claim correctness is defined by more than end-point results.
- We propose three pillars to quantitatively evaluate statistical robustness.
  - Inform user: characterize existing hardware
  - Inform HW designer: design space exploration
- A design might have good end-point results but compromised statistical properties.
- Slight increase in precision achieves FP64 results.
- For broader use: appropriately address uncertainty.

